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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/741,673	12/19/2000	Neil C. Bird	GB 000057	7056
	590 09/26/2003			
PHILIPS INTELLECTUAL PROPERTY & STANDARDS P.O. BOX 3001			EXAMINER	
	MANOR, NY 10510		NGUYEN, LINH M	
			ART UNIT	PAPER NUMBER
			2816	
			DATE MAILED: 09/26/2003	

Please find below and/or attached an Office communication concerning this application or proceeding.

	Application No.	Applicant(s)	Applicant(s)	
Office Action Summary	09/741,673	09/741,673 BIRD, NEIL C.		
omec Action Summary	Examiner	Art Unit	Art Unit	
The MAILING DATE of this	Linh M. Nguyen	2816	13	
The MAILING DATE of this c mmunication Period for Reply	n appears n the cover sheet w	vith the correspondence a	ddress	
A SHORTENED STATUTORY PERIOD FOR RITHE MAILING DATE OF THIS COMMUNICATION - Extensions of time may be available under the provisions of 37 CF after SIX (6) MONTHS from the mailing date of this communication. - If the period for reply specified above is less than thirty (30) days, and the period for reply is specified above, the maximum statutory period for reply within the set or extended period for reply will, by some carned patent term adjustment. See 37 CFR 1.704(b).	ON. FR 1.136(a). In no event, however, may a n. a reply within the statutory minimum of thi eriod will apply and will expire SIX (6) MOI	reply be timely filed rty (30) days will be considered time NTHS from the mailing date of this	ely. communication.	
Status Status		•		
1) Responsive to communication(s) filed on	01 July 2003 and 28 April 200	03 .		
2a) ☐ This action is FINAL. 2b) ☑				
3) Since this application is in condition for all closed in accordance with the practice uno Disposition of Claims	dei <i>Ex parte Quayle</i> , 1935 C.	tters, prosecution as to tl D. 11, 453 O.G. 213.	ne merits is	
4) Claim(s) 1-15 is/are pending in the applica				
4a) Of the above claim(s) is/are with	drawn from consideration.			
5)⊠ Claim(s) <u>1-5 and 11-13</u> is/are allowed.				
6)⊠ Claim(s) <u>6-10,14 and 15</u> is/are rejected.				
7) Claim(s) is/are objected to.				
8) Claim(s) are subject to restriction an Application Papers	d/or election requirement.			
9) The specification is objected to by the Exam	iner.		•	
10)⊠ The drawing(s) filed on 29 May 2001 is/are:		to by the Evaminar		
Applicant may not request that any objection to	the drawing(s) be held in abeva	nce See 37 CEP 1 85/a)		
11) The proposed drawing correction filed on	is: a)□ approved b)□ di	Sapproved by the Examina	nr.	
If approved, corrected drawings are required in	reply to this Office action.	oupprovod by the Examine	71.	
12)☐ The oath or declaration is objected to by the	Examiner.			
Priority under 35 U.S.C. §§ 119 and 120				
13) Acknowledgment is made of a claim for fore	ign priority under 35 U.S.C. &	119(a)-(d) or (f)		
a)⊠ All b)□ Some * c)□ None of:	, , , , , , , , , , , , , , , , , , ,	110(d)*(d) 01 (l).		
1. Certified copies of the priority docume	ents have been received			
2. Certified copies of the priority docume		nlication No		
3. Copies of the certified copies of the pr	iority documents have been r	eceived in this Notice of	M =	
* See the attached detailed Office action for a list	st of the certified copies not re	eceived.		
14) Acknowledgment is made of a claim for domes	stic priority under 35 U.S.C. §	119(e) (to a provisional a	application)	
 a) The translation of the foreign language p 15) Acknowledgment is made of a claim for domestachment(s) 	rovisional application has bee	an rocciued	rpp no accord.	
Notice of References Cited (PTO-892) Notice of Draftsperson's Patent Drawing Review (PTO-948) Information Disclosure Statement(s) (PTO-1449) Paper No(s)		mmary (PTO-413) Paper No(s) ormal Patent Application (PTO-	 152)	
Patent and Trademark Office OL-326 (Rev. 04-01) Office A	Action Summary			

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DETAILED ACTION

This is a response to the Applicant's RCE filing on 07/01/2003 and amendment submitted on 04/28/2003. According to this amendment, claims 1-15 are now presented in the instant application.

RCE acknowledgement/ Prosecution reopened

1. A request for continued examination under 37 CFR 1.114, including the fee set forth in 37 CFR 1.17(e), was filed in this application after allowance or after an Office action under Ex Parte Quayle, 25 USPQ 74, 453 O.G. 213 (Comm'r Pat. 1935). Since this application is eligible for continued examination under 37 CFR 1.114, and the fee set forth in 37 CFR 1.17(e) has been timely paid, prosecution in this application has been reopened pursuant to 37 CFR 1.114. Applicant's submission filed on 07/01/2003 has been entered.

Claim Rejections - 35 USC § 102

2. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless -

- (b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.
- 3. Claims 6-10 and 14-15 are rejected under 35 U.S.C. 102(b) as being anticipated by Bird (U.S. Patent No. 5,721,422).

With respect to claim 6, Bird discloses, in figure 1 and col. 6, lines 13-39, an electric device comprising (1) an array of charge storage elements which (i) are arranged in rows [N,N',...] and columns [M,M+1,...], and (ii) are coupled to row and column conductors [21, 11]; wherein the column conductors are arranged in groups [11a, 11b, ..] (see figure 7) and each

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group has a respective common output [nodes on 11a]; (2) a multiplexer circuit [S1, S2, 8, capacitor in parallel with 8], for switching a selected one of a plurality of current inputs carried by respective input lines [21a, 21b] to the common output [nodes on 11a], having, for each input line, (i) a diode clamp with first and second clamp terminals [7,6] and first and second clamp diodes [S1,S2] arranged in series with the same polarity between said clamp terminals, and (ii) isolation means [8] between each input line and the common output; wherein (a) each input line is connected to the isolation means and to a point [5'] between the first and second clamp diodes, and (b) the diode clamp operates (i) in a first mode in which voltages are applied to the clamp terminals such that the diodes of the diode clamp are forward biased and hold a first voltage that prevents the passage of current from the input line to the common output, and (ii) in a second mode in which the diodes of the diode clamp are reverse biased allowing for the passage of a current from the input line to the common output, and (c) the multiplexer circuit couples the column conductors of the respective groups to the respective common output; and (3) a charge measurement device [70] that measures a flow of charge from the common output; and wherein only two connections [7, input of S2] to the diode clamp control switching of the respective input line.

With respect to claim 7, figure 1 of Bird shows that the charge storage elements have photosensitive pixels including a photodiode [8] and a switching diode [S1, S2].

With respect to claim 8, figure 1 of Bird shows that the isolation means has an isolation capacitor [capacitor in parallel with 8].

With respect to claim 9, Bird discloses, in figure 1 and col. 8, lines 3-23, that (i) the second mode charge flows from each input to the respective isolation capacitor, and (ii) the diode

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clamp is operable in a third mode in which voltages are applied to the clamp terminals [6, 7] such that the diodes [S1, S2] in the diode clamp are forward biased and hold the input line at a second voltage which causes charge stored on the isolating capacitor to flow between the isolation capacitor and the charge measurement device [70].

With respect to claims 10 and 14-15, figure 1 of Bird shows that the first voltage is selected to reverse bias the switching diode (see col. 6, lines 37-38).

Allowable subject matter

4. Claims 1-5 and 11-13 are allowed.

The following is a statement of reasons for the indication of allowable subject matter:

Prior art fails to disclose or suggest 1) a multiplexer circuit for switching wherein each input line is connected *only* to the isolation means and to a point between the first and second clamp diodes, as called for in claim 1; and 2) the configuration of a variable capacitor in the pixels and that the determination of the capacitance is based on the measured current, as called for in claim 11.

Remarks and conclusion

5. Applicant's arguments with respect to claims 1 and 6 have been seriously considered. However, they are not fully persuasive.

With respect to the Applicant's argument on claim 6, at page 5, 2nd paragraph, the Examiner does not understand what the Applicant intends to argue about.

With respect to claim 6, in last paragraph of page 4 and continued onto page 5, the amendment regarding the added limitation "wherein only two connections to the diode clamp control switching of the respective input line" has not placed the claim in the condition for

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allowance since the cited prior art Bird discloses the indicated limitation (see 102 rejection).

Therefore, claim 6 remains rejected as being anticipated by Bird (U.S. Patent No. 5,721,422).

Inquiry

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Linh M. Nguyen whose telephone number is (703) 305-0414. The examiner can normally be reached on Alternate Mon, Tuesday - Friday from 7:00 to 4:30.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Timothy P Callahan can be reached on (703) 308-4876. The fax phone number for the organization where this application or proceeding is assigned is (703) 872-9306.

Any inquiry of a general nature or relating to the status of this application or proceeding should be directed to the receptionist whose telephone number is (703) 308-0956.

Linh M. Nguyen Examiner

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LMN

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